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DATE: February 1, 2005

PTO IDENTIFIER: Application Number 10/604,609-Conf. #1608
Patent Number

Inventor: James W. Adkisson et al.

MESSAGE TO: US Patent and Trademark Office

FAX NUMBER: (703) 872-9306

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Attorney Dkt. #: 21806-00152-US

PAGES (Including Cover Sheet): 5

CONTENTS: Response to Restriction Requirement dated 01/03/2005 (with Traverso) (3 pages)
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PTO/SB/97 (09-04)

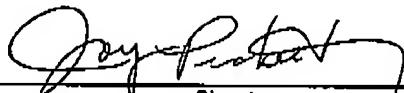
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Application No. (if known): 10/604,609

Attorney Docket No.: 21806-00152-US

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FEB 01 2005

Docket No.: 21806-00152-US
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
James W. Adkisson et al.

Application No.: 10/604,609

Confirmation No.: 1608

Filed: August 4, 2003

Art Unit: 2832

For: ELECTRICALLY TUNABLE ON-CHIP
RESISTOR

Examiner: K. D. Easthom

RESPONSE TO RESTRICTION REQUIREMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the restriction requirement set forth in the Office Action mailed January 3, 2005 (Paper No. 20041221), applicant hereby provisionally elects Group I, Claims 1-10, drawn to a resistor and heater, classified in class 338, subclass 23 for continued examination, with traverse.

The outstanding Office Action has required restriction between:

Group I, Claims 1-10, drawn to a resistor and heater, classified in class 338, subclass 23;
and

Group II, Claims 11-20, drawn to method of operating a resistor and heater, classified in class 219, subclass 446.1.

Applicants make these elections based on the understanding that Applicants are not prejudiced against filing one or more divisional applications that cover the non-elected claims.